

EUROPEAN PATENT OFFICE

Patent Abstracts of Japan

PUBLICATION NUMBER : 2000078850
PUBLICATION DATE : 14-03-00

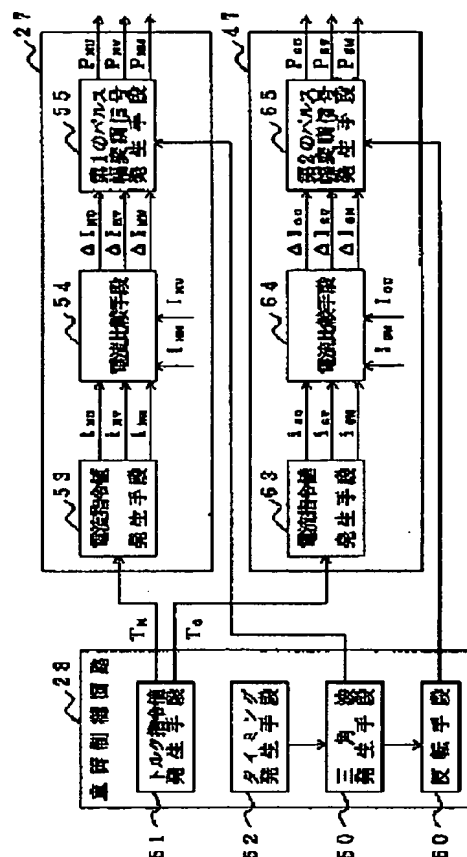
APPLICATION DATE : 31-08-98
APPLICATION NUMBER : 10245383

APPLICANT : AISIN AW CO LTD;

INVENTOR : MAKI KIMIYA;

INT.CL. : H02M 7/48 H02M 7/5387 H02M
7/5395 H02P 7/63

TITLE : INVERTER DEVICE AND CONTROL
METHOD THEREFOR



ABSTRACT : PROBLEM TO BE SOLVED: To extend the life of a smoothing capacitor and reduce the size of an inverter device.

SOLUTION: A 1st current command signal generating means which generates a 1st current command signal for driving a motor, a 1st pulse width modulation signal generating means 55 which generates 1st pulse width modulation signals PMU, PMV and PMW, a 2nd current command generating means which generates a 2nd current command signal for driving a generator and a 2nd pulse width modulation signal generating means 65 which generates 2nd pulse width modulation signals PGU, PGV and PGW are provided. The 1st and 2nd pulse width modulation signal generating means 55 and 65 generate the 1st pulse width modulation signal PMU, PMV and PMW and the 2nd pulse width modulation signals PGU, PGV and PGW with different ON/OFF timings. It can be avoided that the transistor of an inverter for a motor and the transistor of an inverter for a generator are turned on/turned off simultaneously.

COPYRIGHT: (C)2000,JPO